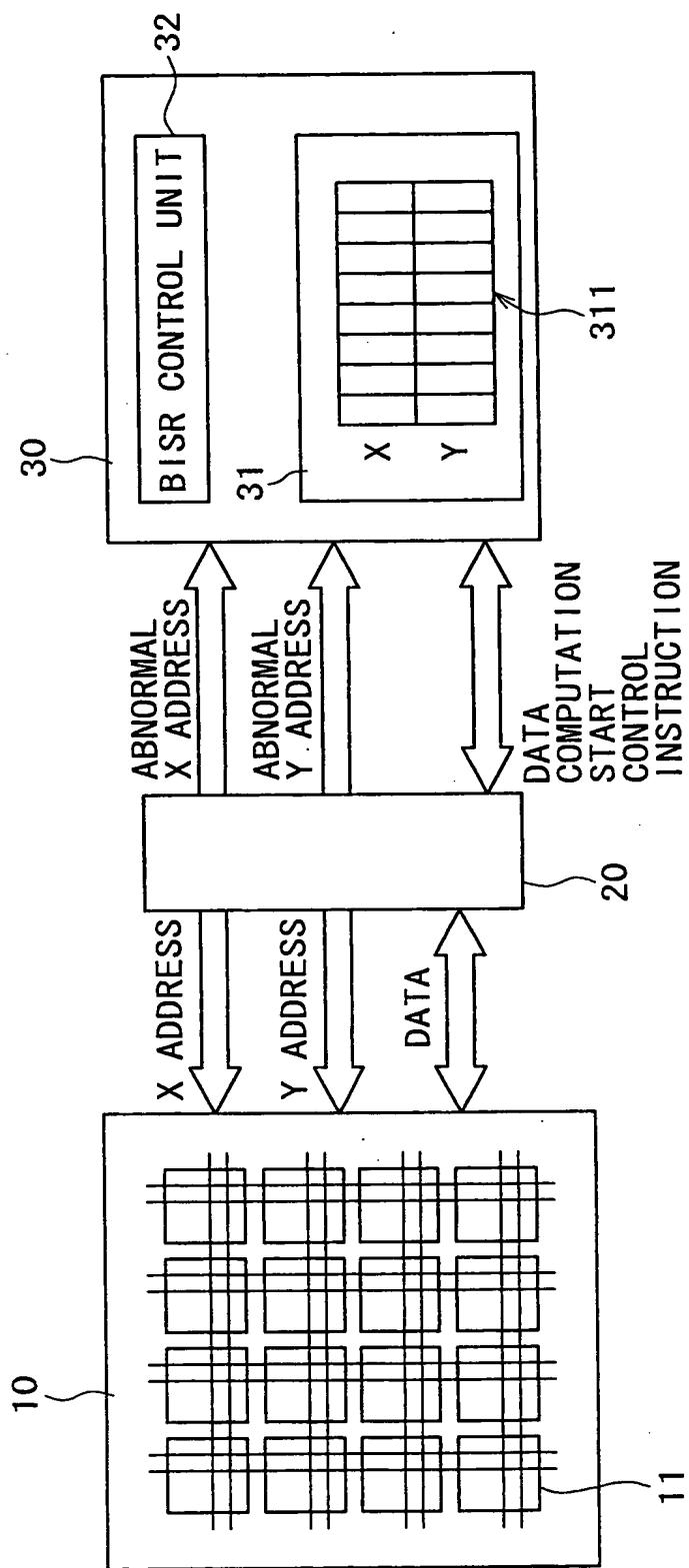
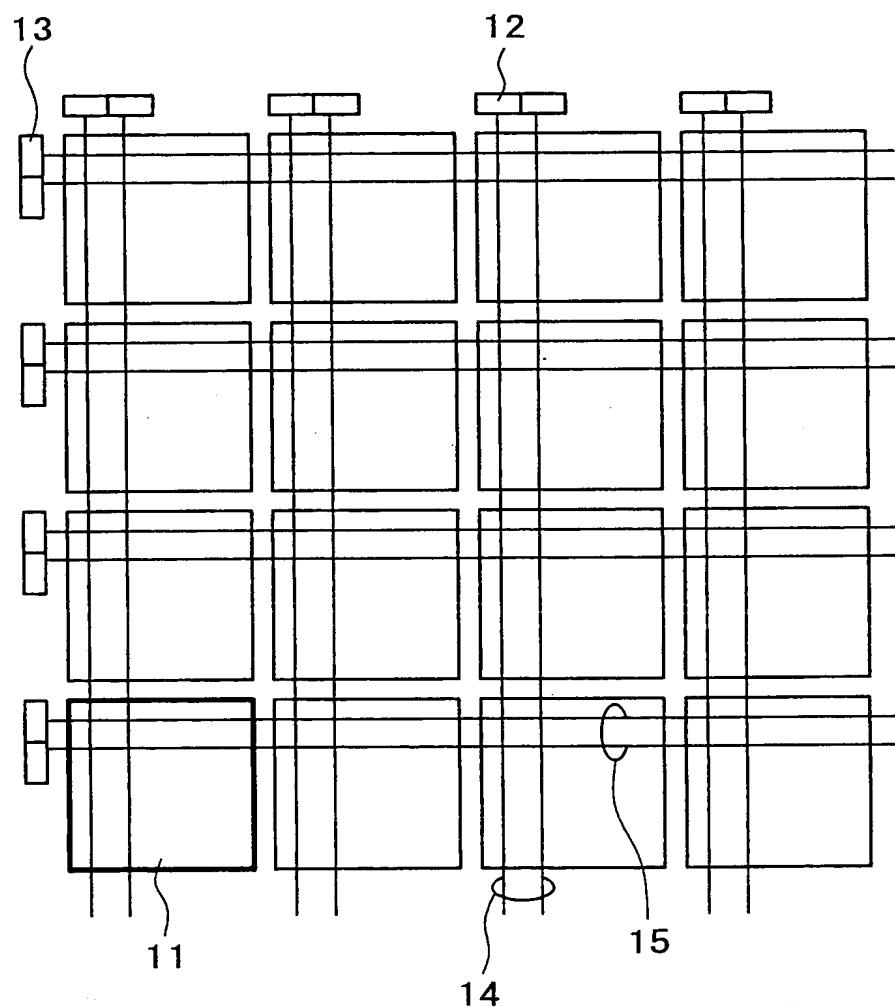


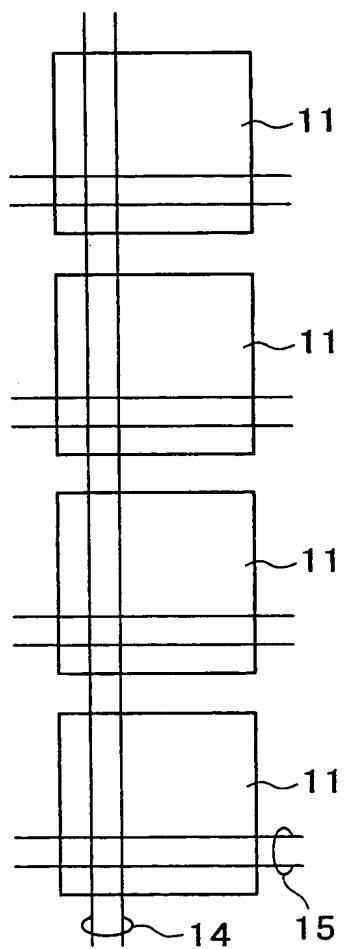
FIG. 1



F I G. 2



F I G. 3



F I G. 4

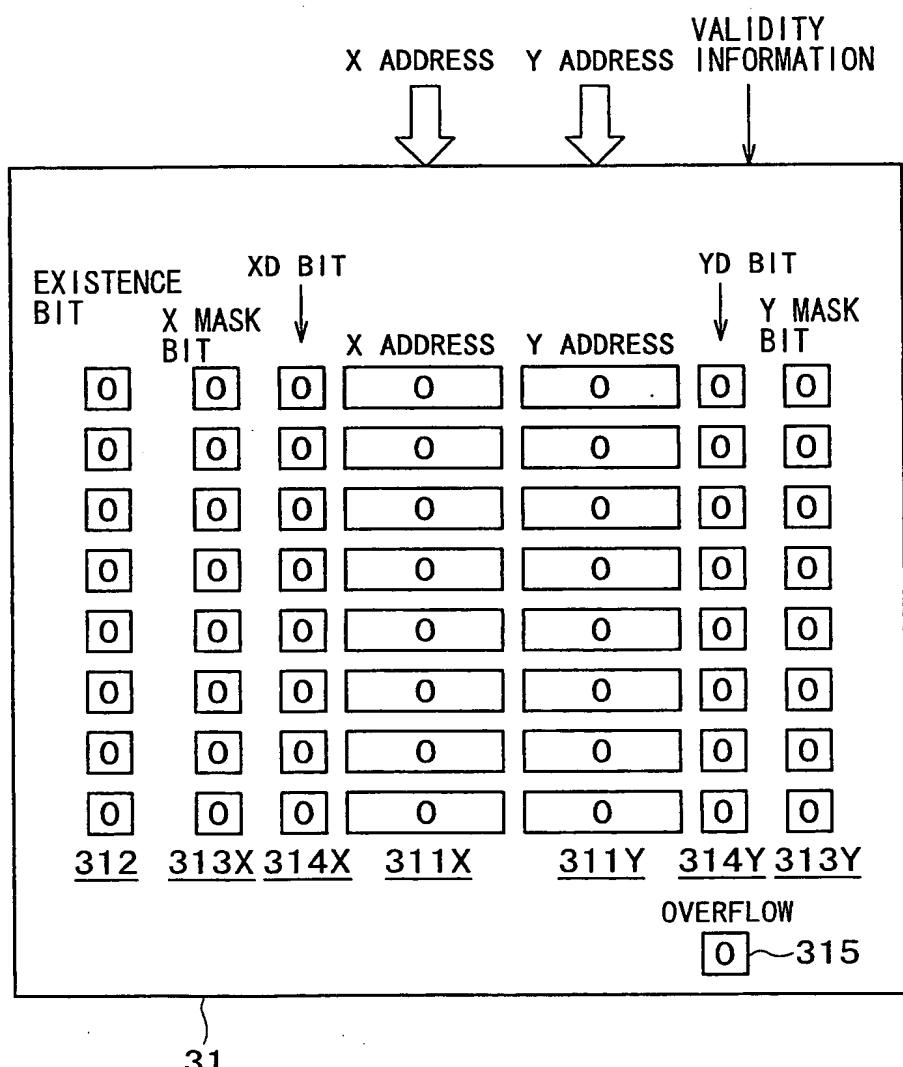


FIG. 5

EXISTENCE BIT	X MASK BIT	XD BIT	X ADDRESS	Y ADDRESS	YD BIT	Y MASK BIT
1	0	0	12	5	0	0
1	0	0	6	5	1	1
1	1	1	12	8	0	0
1	0	0	5	35	0	0
1	0	1	6	35	1	0

F I G. 6

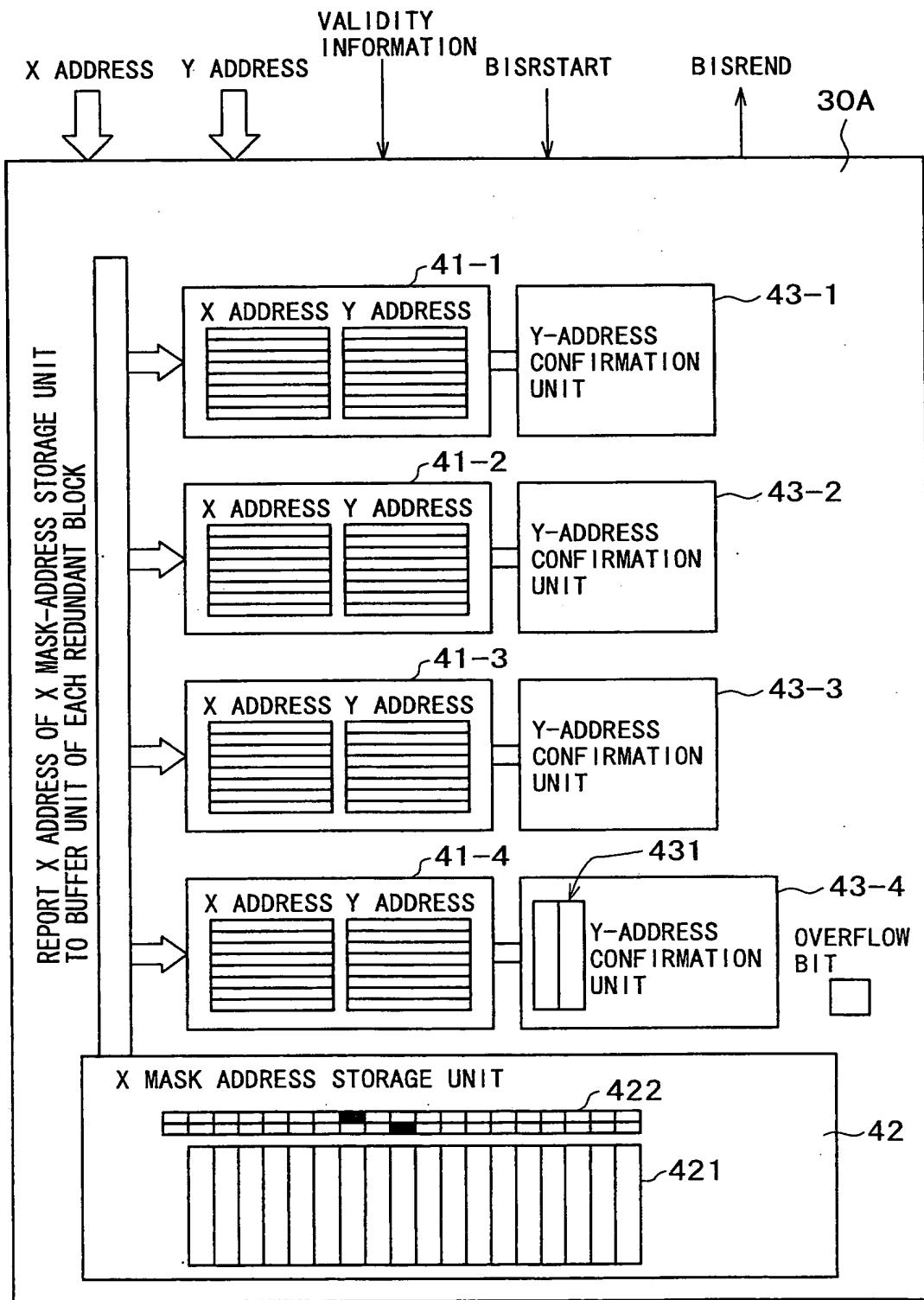
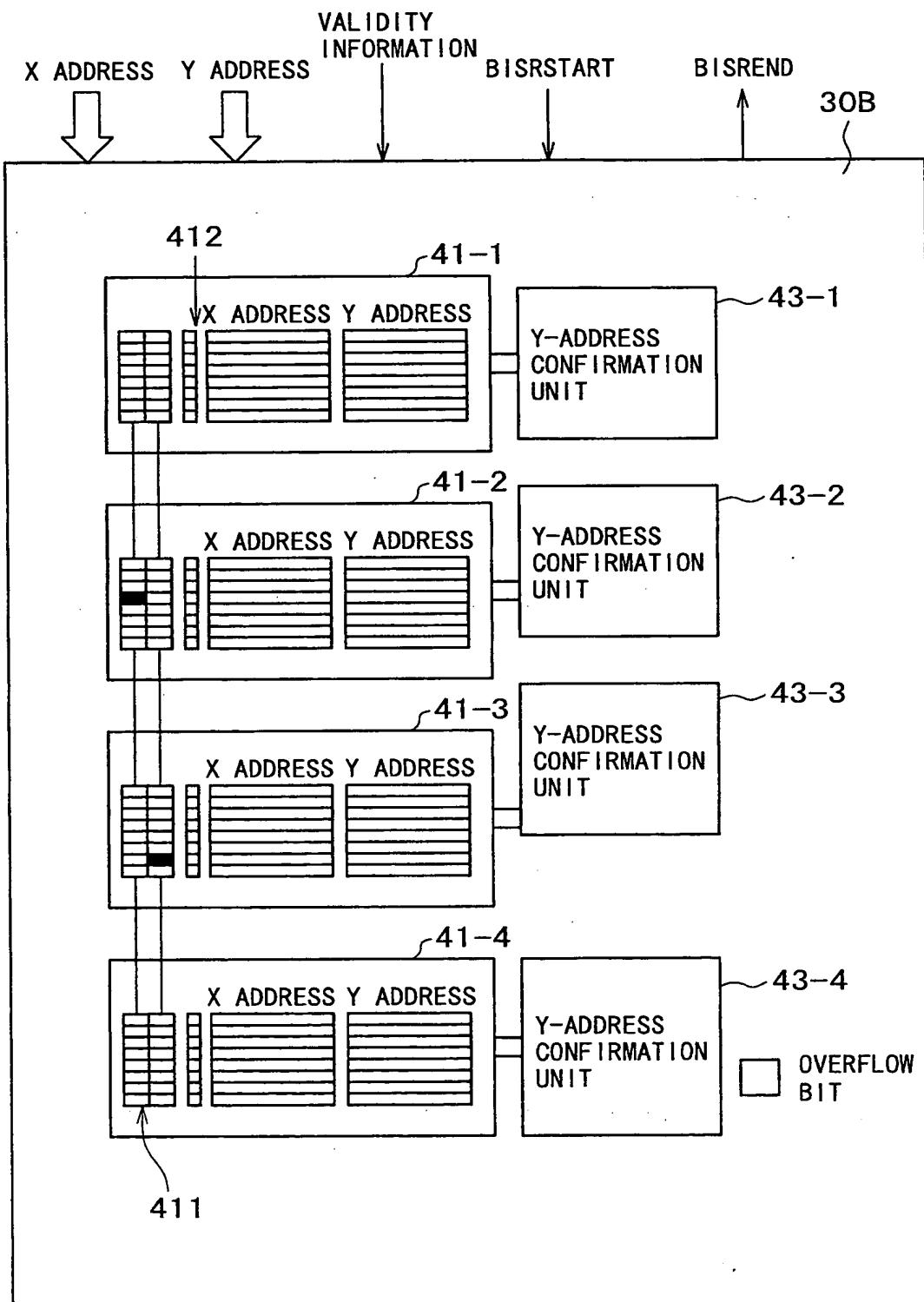
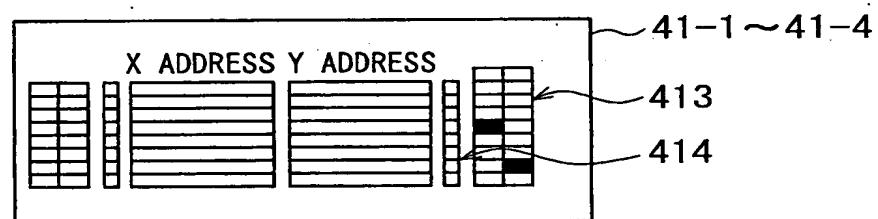


FIG. 7

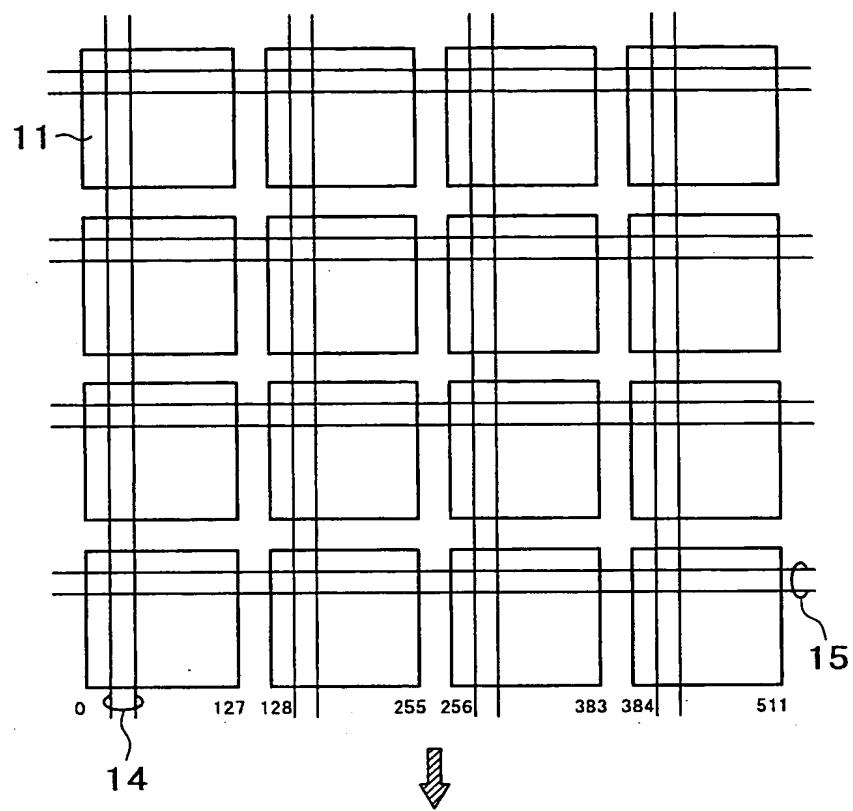


F I G. 8



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F I G. 9 A



F I G. 9 B

